

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	657	(714/42).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 11:35
S25	45	hard with disk with controller with testing	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 18:09
S26	1	microcontroller with "dual RAM"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 18:09
S27	1	microcontroller same "dual RAM"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 18:10
S30	7	microcontroller and "dual RAM"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 18:13
S31	127	microcontroller and "dual-port RAM"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 18:13
S32	41	microcontroller same "dual-port RAM"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 18:57
S33	1	(microcontroller and "dual-port RAM") same test\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 18:58
S34	2546	microcontroller with test\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 19:50
S35	74	microcontroller with test\$4 with address	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 20:13
S36	1	microcontroller with test\$4 with address and dual-port	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 19:51

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S37	469	microcontroller with test\$4 with mem\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 20:13
S38	261	microcontroller with test\$4 with mem\$4 and store	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 20:15
S39	33	microcontroller with test\$4 with mem\$4 and store with address	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/07/28 20:15
S40	769	primary with storage same test\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 11:35
S41	125	primary with storage same test\$4 and (microcontroller or microprocessor)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 11:36
S42	87	primary with storage same test\$4 and (microcontroller or microprocessor) and (error or fault or fail\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 11:36
S43	27	primary with storage same test\$4 and (microcontroller or microprocessor) and (error or fault or fail\$4) and dual	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 14:53
S44	5493	dual with port with ram	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 14:53
S45	116	dual with port with ram and (stor\$4 with (defect or error or fault or fail\$4) with address\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 16:03
S46	2478	(RAID or NAS) with controller and (defect or error or fault or fail\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 16:04
S47	1029	(RAID or NAS) with controller same (defect or error or fault or fail\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 16:04

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S48	828	(RAID or NAS) near5 controller same (defect or error or fault or fail\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 16:04
S49	777	(RAID or NAS) near4 controller same (defect or error or fault or fail\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 16:04
S50	750	(RAID or NAS or "hard disk") near4 controller with (defect or error or fault or fail\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 16:05
S51	44	(RAID or NAS or "hard disk") near4 controller with microcontroller and (defect or error or fault or fail\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 17:59
S52	2	("6314480").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 19:00
S53	2	("4979055").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	OFF	2006/08/14 19:01